

Programm 24. PARS Workshop 26.+27.05.2011 Rüschnikon

Donnerstag 26.05.2011

- 12:30 - 12:35 Opening, *A. Döring, Rüschnikon*
- 12:35 - 13:30 Invited Talk: A Portable Approach to Multicore Programming
J. Labarta, Barcelona Supercomputing Center
- 13:30 - 15:00 Session 1: Parallel Programming and Models (Chair: H. Weberpals)
- A Benchmark Suite for Evaluating Parallel Programming Models: Introduction and Preliminary Results, *M. Andersch, B. Juurlink, C.C. Chi, Berlin*
 Run, Stencil, Run! -- A Comparison of Modern Parallel Programming Paradigms
H. Burkhardt et al, Basel
 Implementing the Global Cellular Automata on CUDA
B. Milde, N. Büscher, M. Goesele, Darmstadt
- 15:00 - 15:20 Pause
- 15:20 - 16:50 Session 2: Hardware (Chair: K. D. Reinartz)
- Synchronization Mechanism of Hardware and High-level Models for Performance Verification, *V. Caparros Cabezas, A. Döring, H. Ineichen, Rüschnikon*
 MANJAC - Ein Many-Core-Emulator auf Multi-FPGA-Basis
C. Bradatsch et al, Augsburg
 A Scalable Hardware Algorithm for Demanding Time-Out Management in Network Systems, *K. Septinus et al, Hannover*
- 16:50 - 17:20 Poster Session
gleichzeitig Treffen PARS-Programmkomitee zur Vergabe des PARS-Preises
- 17:20 - 17:50 Sitzung der Fachgruppe PARS
- 18:00 Transfer zum Restaurant (15min)
- 18:30 Abendveranstaltung mit Verleihung des PARS-Preises, anschließend Transfer zum Bahnhof Thalwil/Sedartis Hotel

Freitag 27.05.2011

- 8:30 - 10:30 Session 3: Theory and Frameworks of Parallel Computing (Chair: P. Sobe)
- Can Computation Be Extended beyond the Von-Neumann-Style?
H. von Issendorff, Hemmoor
 Is There Hope for Automatic Parallelization of Legacy Industry Automation Applications? *S. Kempf, R. Veldema, M. Philippsen, Erlangen*
 Self-organized Invasive Parallel Optimization with Self-repairing Mechanism
S. Mostaghim, F. Pfeiffer, H. Schmeck, Karlsruhe
 Peelsched: a Simple and Parallel Scheduling Algorithm for Static Taskgraphs
J. Keller, Hagen
- 10:30 - 10:45 Pause

10:45 - 12:45

Session 4: Applications (Chair: H. Burkhardt)

An advanced Cluster-Architecture for the Exascale

N. Eicker, T. Lippert, Jülich

An OpenCL-based Parallel Coder for Erasure-tolerant Storage

P. Hegen, P. Sobe, Dresden

A SSE-based Implementation of a Ray Tracer for the Comparison with

Coprocessors in Hybrid Computing Systems, *V. Hampel, E. Maehle, Lübeck*

Efficient Synchronization Techniques in a Decentralized Memory Management

System Enabling Shared Memory, *O. Mattes et al, Karlsruhe*

12:45 - 13:00

Closing Remarks, Invitation to PARS 2012 (Munich with ARCS 2012), *A. Döring*